

PRB018 Programming Board for POSIC SMD encoders

Product data

Features

OTP programming of POSIC's 18-pin SMD encoders IT5602 and AP5603

Key Specifications

Description

The Programming Board is intended for OTP-programming of POSIC's 18-pin SMD encoders IT5602 and AP5603 prior to soldering. It is compatible to the Interface Board of the Evaluation and Programming Tool EPT002.

Fig 1 shows how to connect the Programming Board to the ASSIST Interface Board.

Fig 2 shows the opened socket containing an 18-pin encoder. To open the socket, the top side of the black lever must be pushed towards the center of the socket. Align the 18-pin encoder in the socket:

- The solder side* (large copper pads) must be facing down.
- The sensing side* (small copper pads) must be facing up.
- The round copper mark must be at the upper right side in Error! Reference source not found. In the socket, this corner has a larger recess/indent than the other 3 corners
- * See IT5602 or AP5603 datasheet Fig 3.



Fig 1 Programming Board (1) connected to ASSIST Interface Board (2), which is connected via USB cable (3) to PC with ASSIST software.



Fig 3 shows the dimensions of the Programming Board and provides the explanations for the three jumpers. Please note that jumpers J8 and J9 (4 and 5 in Fig 3) must be set/removed according to the type of encoder (IT5602 or AP5602).

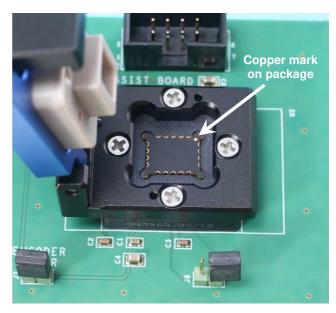


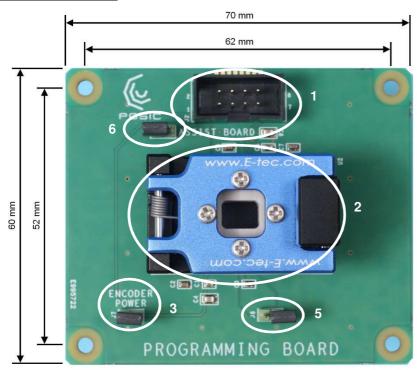
Fig 2 Programming Board, socket with lid open.

Ordering information

PRB018 Programming board with socket for 18-pin encoders IT5602 and AP5603 including flat cable for connection to ASSIST Interface Board.

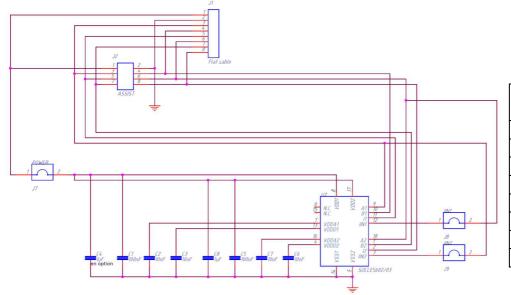
Programming Board

Technical drawings



- 8-pin DIN41651 connector to ASSIST Interface Board
- 2) Socket
- 3) Encoder Power jumper J7
 - Present: encoder powered via ASSIST Interface Board connector
 - Removed: encoder not powered (can be powered via jumper-pin)
- 4) Jumper J8 must be removed
- 5) Jumper J9:
 - Present for operation with IT5602
 - Removed for operation with AP5603

Fig 3 Dimensions and explanations.



Pin	ASSIST Interf. Board	
	IT5602	AP5603
1	VDD, 5V Supply	
2	VSS, Ground	
3	A1	CLOCK1
4	B1	DATA1
5	l1	NC
6	A2	CLOCK2
7	B2	DATA2
8	12	NC

Fig. 4 Schematic diagram.

Fig 6 Pinout connectors J1, J2.

© Copyright 2018 POSIC. All rights reserved. No part of this document may be reproduced without the prior written consent of POSIC. Products and companies mentioned in this document may be trademarks or registered trademarks of their respective owners. Information in this document is believed to be accurate and reliable and may be changed without notice. No responsibility is assumed by POSIC for its use, nor for infringements of patents or other rights of third parties which may result from its use. Some commercial or geographical restrictions may apply to the sale of this product. No license is granted by implication or otherwise under any patent or patent rights of POSIC. In no event shall POSIC, its employees, its contractors, or the authors of this document be liable for special, direct, indirect, or consequential damage, losses, costs, charges, claims, demands, claim for lost profits, fees, or expenses of any nature or kind. POSIC products are not designed, intended, authorized or warranted to be suitable for use in life-support applications, devices or systems or other critical care applications. Inclusion of POSIC products in such applications is understood to be undertaken solely at the customer's own risk. Should a customer purchase or use POSIC products for any such unauthorized application, the customer shall indemnify and hold POSIC and its officers, employees, subsidiaries, affiliates, representatives and distributors harmless against all claims, costs, damages and attorney fees which could arise.